

JC20 Rec'd PG7/FTC 2 7 OCT 2005

Application Data Sheet

Application Information

Application Type:: Regular

Subject Matter:: National Phase

Suggested Group Art Unit:: N/A

CD-ROM or CD-R?:: None Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: LOW-VOLTAGE IC-CIRCUIT

Attorney Docket Number:: 4436-0113PUS1

Request for Early Publication?:: No
Request for Non-Publication?:: No

Total Drawing Sheets:: 5

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

Applicant Information

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Switzerland

Status:: Full Capacity
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Family Name:: KAESLIN

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Applicant Authority Type:: Inventor

Primary Citizenship Country::

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Status::

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Correspondence Information

Correspondence Customer Number::

02292

Representative Information

Representative Customer Number::

02292

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/EP2004/004433	04/27/04

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
	03388028.7	04/30/03	Yes
Zaropouri, arom z			

Assignee Information

Assignee name::

Bernafon AG

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